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M.L.PCT PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (DO/EO/US)**
(motoro01.001)

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Applicant: T. Brightman, et al. **Paper No.:****Application No:** PCT/US99/10002 **Group Art Unit:**10 **Filed:** (Int. filing date 7 MAY 99) **Examiner:** *✓***Title:** *Digital Communications Processor*15 Assistant Commissioner for Patents
Washington, DC 20231**Preliminary Amendment**

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The above patent application was the subject of an International Preliminary Examination under PCT Chapter II in which claims 1-10 and 14-20 were found to meet the criteria set out in PCT Article 33(2)-(4). Claims 11-13 were not examined. Applicants are hereby replacing the above claims with a new set of claims 1-10.

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Please ~~cancel~~ all claims presently in the application without prejudice and replace them with the following new claims 1-10:

P1.126 14.

1 ~~14.~~ An integrated circuit comprising:
2 a plurality of data stream inputs and/or outputs that receive and/or transmit streams of
3 data;
4 a plurality of data stream processors that process the streams of data, each data stream
5 processor being coupled to a data stream input and/or data stream output and
6 including
7 a writeable instruction memory containing instructions and
8 a control data processor that controls the data stream processor by sequentially
9 executing instructions from the writeable instruction memory.

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P1.126 15.

1 ~~15.~~ The integrated circuit set forth in claim ~~1~~ ¹⁴ wherein: